

Digital Logic

Course Title: Digital Logic
Course No: BIT103
Nature of the Course: Theory + Lab
Semester: I

Full Marks: 60 + 20 + 20
Pass Marks: 24 + 8 + 8
Credit Hrs: 3

Course Description:

This course familiarizes students with Number System, Digital Design Fundamentals, Understand and Design Functions of Combinational Logic, Sequential Logic (Counters, Registers and Finite State Machine), Memories, Programmable Logic Devices Integrated Circuit Technologies.

Course Objective:

To provide the concepts used in the design and analysis of digital systems and introduces the principles of digital computer organization and design.

Course Contents:

Unit 1: Number Systems, Operations and Codes (6 Hrs.)

Introduction to Number System, Decimal, Binary, Octal, Hexadecimal Number Systems, Conversion from one number system to another, Complements of Numbers, Addition and Subtraction of Binary Numbers, Binary Codes and Error Detection Codes

Unit 2: Digital Design Fundamentals and Boolean algebra (8 Hrs.)

Digital and Analog Signals, Logic Operations, Introduction to the System Concept, Logic Gates (Basic Gates, Derived Gates, Universal Gates), Logic Function and Boolean Algebra

Unit 3: Simplification of Boolean Functions (5 Hrs.)

K-map, Two and Three variable maps, Four variable maps, product of sum simplification, NAND and NOR implementation, Don't Care conditions

Unit 4: Combinational Logic (7 Hrs.)

Adders and Subtractors, Parallel Binary Adders, Multiplexers and Demultiplexers, Encoders and Decoders, Seven segment decoder, Code Converters

Unit 5: Sequential Logic (4 Hrs.)

Latches, Edge-Triggered Flip-Flops, Flip-Flop Operating Characteristics, Flip-Flop Application

Unit 6: Counters, Registers and Memory (9 Hrs.)

Asynchronous Counters, Synchronous Counters, Up/Down Counters, Counter Applications, Basic Shift Register Operations, Shift Register Types, Bidirectional Shift Registers, Shift Register Counters, Basic Memory Operations and memory types

Unit 7: Processor Logic Design (6 Hrs.)

Processor Organization, Arithmetic Logic Unit, Design of Arithmetic Circuit, Design of Logic Circuit, Design of Arithmetic Logic Unit, Status Register, Design of Shifter

Laboratory Works:

- Familiarization with Logic Gates
- Encode and Decodes
- Multiplexer and De-Multiplexer
- Design of simple combination Circuits
- Design of Adder/combination Circuits
- Design of Flip Flop
- Clock driven sequential circuits
- Conversion of parallel data into serial format
- Generation of timing signal for sequential system

Text Book:

- Mano M.M., *Digital logic and Computer Design*, Pearson Education

References Books:

- Mano M.M. and Ciletti M. M, *Digital Design*, 4th edition
- Brown S. and Vranesic Z., *Fundamentals of Digital Logic with VHDL Design*, 3rd edition, McGraw Hill
- Rafiquzzaman M., *Fundamentals of Digital Logic and Microcomputer Design*, 5th edition, JohnWiley & Sons, Inc.
- Holdsworth B. and Woods C., *Digital Logic Design*, 4th edition
- Mano M. M, Kime C. R , *Logic and computer design fundamentals*, 2nd edition